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IN THE CLAIMS

*The status of the claims as presently amended is as follows:*

1. *(Currently Amended)* An intelligent power module comprising:
  - power semiconductor switching devices;
  - driving means for driving said switching devices;
  - abnormal condition detecting means for detecting a fatal abnormal condition and a precursory abnormal condition thereof in said switching devices or said driving means;
  - protecting means for protecting said switching devices when said abnormal condition detecting means detects the abnormal condition; and
  - abnormal condition output means for outputting signals based on abnormal condition detecting signals outputted from said abnormal condition detecting means to outside;wherein said abnormal condition detecting means comprises:
  - at least one output terminal for outputting alarm signals when said abnormal condition detecting means detects the fatal abnormal condition; ~~and an output terminal~~ and for outputting abnormality factor discrimination signals indicating abnormality factors contributing to the fatal abnormal condition and the precursory abnormal condition.
2. *(Original)* An intelligent power module according to claim 1, wherein said abnormality factor discrimination signals are outputted as serial signals.
3. *(Currently Amended)* An intelligent power module according to claim 1, wherein said at least one output terminal for outputting the alarm signals and ~~said output terminal~~ for outputting the abnormality factor discrimination signals are the same terminal.
4. *(Currently Amended)* An intelligent power module according to claim 1, further including at least one input terminal for inputting an abnormality factor output request signal from outside, and wherein:
  - said abnormality factor discrimination signals are outputted in synchronism with ~~an~~ the

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abnormality factor output request signal inputted from outside.

5. (*Original*) An intelligent power module according to claim 4, wherein:

said abnormality factor output request signal includes a transmission preparation signal having a predetermined cycle, said transmission preparation signal updating the abnormality factor discrimination signals stored in parallel-in/serial-out shift registers in said abnormal condition output means, and a read clock following said transmission preparation signal causes said shift registers to output the abnormality factor discrimination signals.

6. (*Currently Amended*) An intelligent power module according to ~~claim 4, wherein~~ comprising:

power semiconductor switching devices;

driving means for driving said switching devices;

abnormal condition detecting means for detecting a fatal abnormal condition and a precursory abnormal condition thereof in said switching devices or said driving means;

protecting means for protecting said switching devices when said abnormal condition detecting means detects the abnormal condition;

at least one input terminal for inputting an abnormality factor output request signal from outside; and

abnormal condition output means for outputting signals based on abnormal condition detecting signals outputted from said abnormal condition detecting means to outside,

wherein said abnormal condition output means comprises at least one output terminal for outputting alarm signals when said abnormal condition detecting means detects the fatal abnormal condition and for outputting abnormality factor discrimination signals indicating abnormality factors contributing to the fatal abnormal condition and the precursory abnormal condition,

wherein if said fatal abnormal condition is developed, said abnormal condition output means outputs alarm signals irrespective of whether said abnormality factor output request signal exists or not, and if said precursory abnormal condition is developed, said abnormal condition

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output means outputs the abnormality factor discrimination signals separately in time in synchronism with said abnormality factor output request signal.

7. (*Currently Amended*) An intelligent power module according to claim 1, wherein:  
if said fatal abnormal condition is developed, said abnormal condition detecting means cuts off said semiconductor switching devices cut-off themselves through said driving means.
8. (*Original*) An intelligent power module according to claim 1, wherein:  
said semiconductor switching devices are connected in single-phase bridge connection.
9. (*Original*) An intelligent power module according to claim 1, wherein:  
said semiconductor switching devices are connected in three-phase bridge connection.
10. (*Currently Amended*) An intelligent power module ~~according to claim 8, wherein~~comprising:  
power semiconductor switching devices;  
driving means for driving said switching devices;  
abnormal condition detecting means for detecting a fatal abnormal condition and a precursory abnormal condition thereof in said switching devices or said driving means;  
protecting means for protecting said switching devices when said abnormal condition detecting means detects the abnormal condition; and  
abnormal condition output means for outputting signals based on abnormal condition detecting signals outputted from said abnormal condition detecting means to outside,  
wherein said abnormal condition output means comprises at least one output terminal for outputting alarm signals when said abnormal condition detecting means detects the fatal abnormal condition and for outputting abnormality factor discrimination signals indicating abnormality factors contributing to the fatal abnormal condition and the precursory abnormal condition.

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wherein said semiconductor switching devices are connected in single-phase bridge connection and wherein the abnormal condition detecting signals from said abnormal condition detecting means corresponding to semiconductor switching devices connected in bridge connection in an upper arm are transmitted to a lower arm through high voltage level shifters.

11. (Original) An intelligent power module according to claim 10, wherein:

said high voltage level shifters perform parallel transmission of multiple abnormal condition detecting signals.

12. (Original) An intelligent power module according to claim 10, wherein:

said multiple abnormal condition detecting signals are encoded and are parallel transmitted by said high voltage level shifters.

13. (Original) An intelligent power module according to claim 10, wherein:

said multiple abnormal condition detecting signals are converted into serial signals and are serial-transmitted by said high voltage level shifters.

14. (Currently Amended) An intelligent power module according to claim 9, wherein comprising:

power semiconductor switching devices;

driving means for driving said switching devices;

abnormal condition detecting means for detecting a fatal abnormal condition and a precursory abnormal condition thereof in said switching devices or said driving means;

protecting means for protecting said switching devices when said abnormal condition detecting means detects the abnormal condition; and

abnormal condition output means for outputting signals based on abnormal condition detecting signals outputted from said abnormal condition detecting means to outside.

wherein said abnormal condition output means comprises at least one output terminal for outputting alarm signals when said abnormal condition detecting means detects the fatal

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abnormal condition and for outputting abnormality factor discrimination signals indicating abnormality factors contributing to the fatal abnormal condition and the precursory abnormal condition.

wherein said semiconductor switching devices are connected in three-phase bridge connection and wherein the abnormal condition detecting signals from said abnormal condition detecting means corresponding to semiconductor switching devices connected in bridge connection in an upper arm are transmitted to a lower arm through high voltage level shifters.

15. *(Original)* An intelligent power module according to claim 14, wherein:

said high voltage level shifters perform parallel transmission of multiple abnormal condition detecting signals.

16. *(Original)* An intelligent power module according to claim 14, wherein:

said multiple abnormal condition detecting signals are encoded and are parallel transmitted by said high voltage level shifters.

17. *(Original)* An intelligent power module according to claim 14, wherein:

said multiple abnormal condition detecting signals are converted into serial signals and are serial-transmitted by said high voltage level shifters.

18. *(Currently Amended)* An intelligent power module comprising:

power semiconductor switching devices;

driving means for driving said switching devices;

abnormal condition detecting means for detecting a fatal abnormal condition and a precursory abnormal condition thereof in said switching devices or said driving means;

protecting means for protecting said switching devices when said abnormal condition detecting means detects the abnormal condition; and

abnormal condition output means for outputting signals based on abnormal condition

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detecting signals outputted from said abnormal condition detecting means to outside;

wherein said abnormal condition ~~detecting~~output means comprises:

at least one output terminal for outputting alarm signals when said abnormal condition detecting means detects the fatal abnormal condition; ~~and an output terminal~~and for outputting warning signals when said abnormal condition detecting means detects the precursory abnormal condition; and

wherein abnormality factors are discriminated according to combinations of output signals from ~~the respective~~said at least one output terminal~~terminal~~.

19. (*Original*) An intelligent power module according to claim 18, wherein:

said semiconductor switching devices are connected in bridge connection, and said alarm signals are generated by obtaining a logical sum of the abnormal condition detecting signals indicating the fatal abnormal condition from said abnormal condition detecting means corresponding to the semiconductor switching devices in an upper arm and the abnormal condition detecting signals indicating the fatal abnormal condition from said abnormal condition detecting means corresponding to the semiconductor switching devices in a lower arm; and

said warning signals are generated by obtaining a logical sum of the abnormal condition detecting signals indicating the precursory abnormal conditions from said abnormal condition detecting means corresponding to the semiconductor switching devices in the upper arm and the abnormal condition detecting signals indicating the precursory abnormal condition from said abnormal condition detecting means corresponding to the semiconductor switching devices in the lower arm.